

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Othmar Leitner, et al. Art Unit : Not Yet Assigned
Serial No. : Not Yet Assigned Examiner : Not Yet Assigned
Filed : Herewith PCT Appln No.: PCT/EP2004/012031
Title : METHOD FOR THE PRODUCTION OF TRANSISTOR STRUCTURES
WITH LDD

Mail Stop Box PCT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Prior to examination, please amend above-identified application as follows:

CERTIFICATE OF MAILING BY EXPRESS MAIL

Express Mail Label No. EL 9800792353 US

May 16, 2006
Date of Deposit